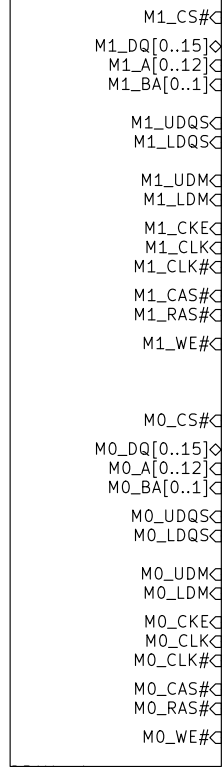
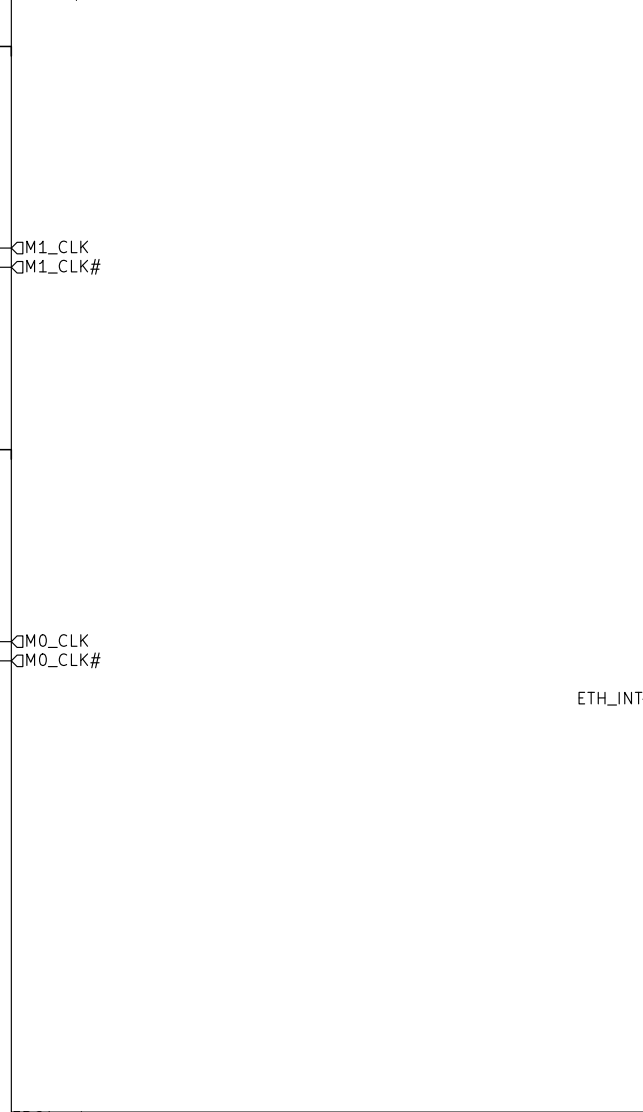


DDR Banks



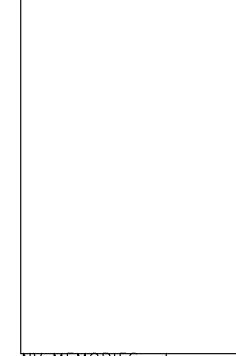
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FPGA Spartan6

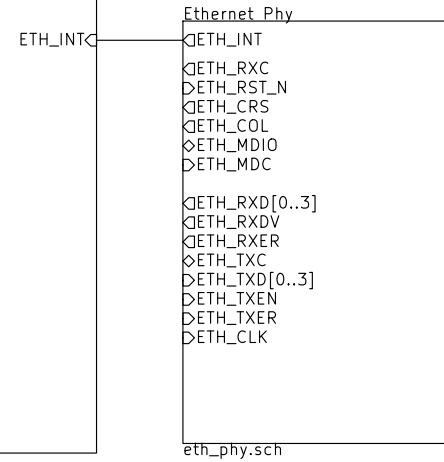


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Non volatile memories

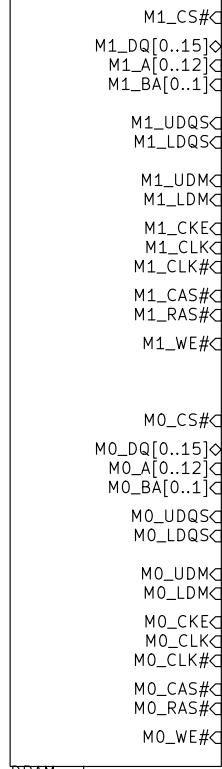


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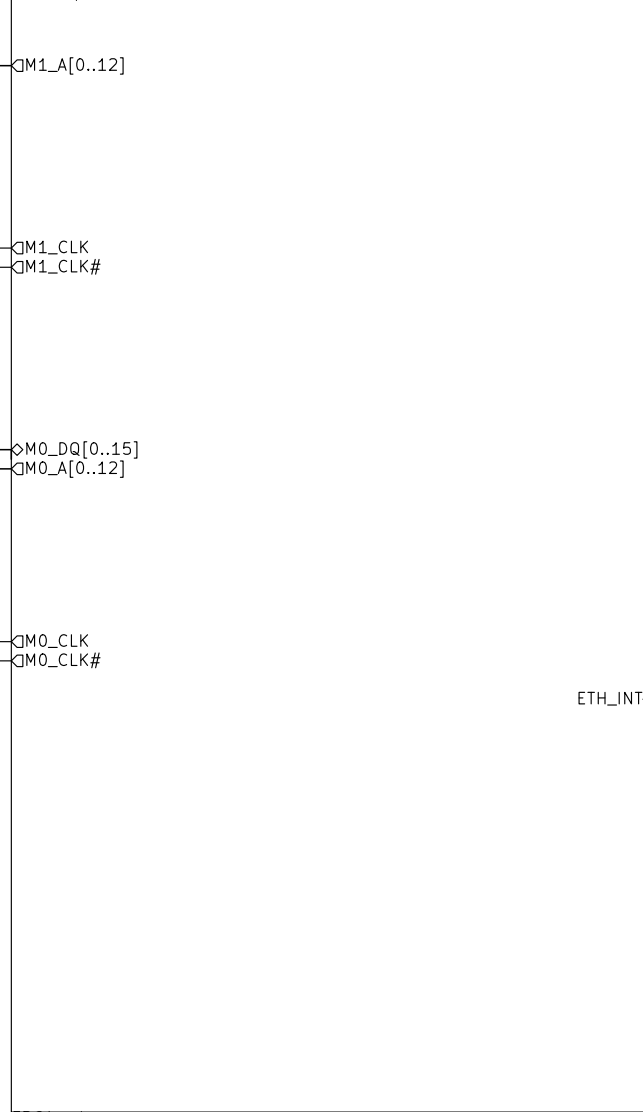
eth_phy.sch

DDR Banks



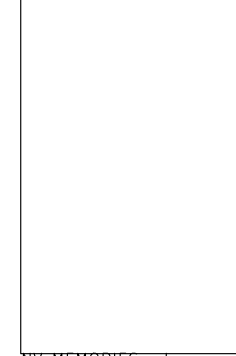
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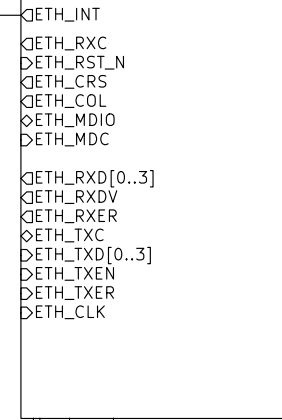


FPGA.sch

Non volatile memories



Ethernet Phy



eth_phy.sch